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OFFICE OF PETITIONS

In re Application of :
Negoro, et al. :
Application No.: 10/769,817 : LETTER
Filed: February 3, 2004 :
Attorney Docket No.: R2180.0189/P189 :
For: SEMICONDUCTOR DEVICE HAVING
DMOS AND CMOS ON SINGLE SUBSTRATE

Dear Mr. Kimura:

You are named as a joint inventor in the above identified United States patent application, filed under the provisions of 35 U.S.C. 116 (United States Code), and 37 CFR 1.47(a), Rules of Practice in Patent Cases. Should a patent be granted on the application you will be designated therein as a joint inventor.

As a named inventor you are entitled to inspect any paper in the file wrapper of the application, order copies of all or any part thereof (at a prepaid cost per 37 CFR 1.19) or make your position of record in the application. Alternatively, you may arrange to do any of the preceding through a registered patent attorney or agent presenting written authorization from you. If you care to join the application, counsel of record (see below) would presumably assist you. Joining in the application would entail the filing of an appropriate oath or declaration by you pursuant to 37 CFR 1.63.

Requests for information regarding your application should be directed to the File Information Unit at (703)308-2733. Information regarding how to pay for and order a copy of the application, or a specific paper in the application, should be directed to Certification Division at (703)308-9726 or 1(800)972-6382 (outside the Washington D.C. area).

Telephone inquiries regarding this communication should be directed to the undersigned at (571) 272-3230.

A handwritten signature in black ink, appearing to read "E. Shirene Willis".

E. Shirene Willis
Senior Petitions Attorney
Office of Petitions

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